

Amendment to the Abstract:

Please replace the Abstract with the following replacement Abstract:

REPLACEMENT ABSTRACT**Abstract of the Disclosure**

An integrated circuit is provided comprising a plurality of master and slave modules and a network arranged for transferring messages between the modules, wherein a message issued by a master module comprises first information indicative for a location of a slave (addressed) module within the network, and second information indicative for a location within the addressed module. The integrated circuit further comprises at least one address translation means for arranging the first and the second information as a single address. The address translation means is adapted to determine which slave module is being addressed based on the single address, and the selected location of the slave (addressed) module is determined based on the single address. Accordingly, use of an address translation means for address mapping allows the design of the master modules to be implemented independent of address mapping.